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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re United States Patent Application of:

Applicant: Michael B. Korzenski et al.

Application No.: 10/602,172

Date Filed: June 24, 2003

U.S. Patent No.: 7,119,052

Date Issued: October 10, 2006

Title: COMPOSITIONS AND METHODS
FOR HIGH-EFFICIENCY
CLEANING/POLISHING OF
SEMICONDUCTOR WAFERS

) Docket No.: 020732-249.643

) Examiner: Gregory Webb

) Art Group: 1751

) Confirmation No.: 9457

) Customer No.: 24239

24239

FIRST CLASS MAIL CERTIFICATE

I hereby certify that I am mailing the attached documents to the Commissioner for Patents on the date specified, in an envelope addressed to the Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450, and First Class Mailed under the provisions of 37 CFR 1.8.

Lillian S. Glenn

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November 28, 2006

Date

Certificate
DEC 06 2006
of Correction

SUBMISSION OF CERTIFICATE OF CORRECTION UNDER THE PROVISIONS OF
37 CFR § 1.322 AND 35 U.S.C. § 254 FOR U.S. PATENT NO. 7,119,052 B2

Commissioner for Patents
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Sir:

DEC 08 2006

Enclosed is a Certificate of Correction submitted under the provisions of 35 U.S.C. §254, to correct errors of the Patent and Trademark Office in the printing of the above-identified U.S. Patent.

Respectfully submitted,



Tristan A. Fuierer

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO: 7,119,052 B2

DATED: October 10, 2006

INVENTOR(S): Michael B. Korzenski et al.

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

Column 5, line 14, "ex periment" should be --experiment--

Column 9, line 19, "99.91%" should be --99.01% --

MAILING ADDRESS OF SENDER:

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PATENT NO. 7,119,052 B2

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